**NAND Flash Bad Block Management Questionnaire**

V1.01

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[8. Please fill below column if there is any programming content for fixed position. And, should Dediprog programmer verdict programmed fail when there is a bad block on the fixed position? 5](#_Toc366255797)

[9. Is there any specific data (Ex: Data mapping table, Buffer data… etc.) need Dediprog self-calculate and write into chip? If yes, please provide more details and advise it is Big or Small Endian. 6](#_Toc366255798)

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# Introduction

**Please fill the whole questionnaire for Dediprog providing complete support on your NAND Flash programming demand.**

**If you have related spec/source code, please provide to Dediprog as well.**

# Question List

### Please advise the bad block management method you use and how it works. Ex: Skip Bad Block, Reserved Area … etc.

**Bad blocks should be skipped.**

**The first 6 blocks must be valid.**

### Please use any diagram to explain the way of your bad block management method clearly.

**The diagram below accurately describes the bad block management scheme that should be used.**

Chip Programming File

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Block 0 |  |  |  | Block 0 |
| 1 |  |  |  | 1 |
| 2 |  |  |  | 2 |
| Block 3 is bad |  |  |  | 3 |
| 4 |  |  |  | 4 |
| 5 |  |  |  | 5 |
| 6 |  |  |  | 6 |
| Block 7 is bad |  |  |  | 7 |
| 8 |  |  |  | 8 |
| 9 |  |  |  | 9 |
| Block 10 is bad |  |  |  | 10 |
| 11 |  |  |  | 11 |
| 12 |  |  |  | 12 |
| 13 |  |  |  | 13 |
| …. |  |  |  | …. |
| n-4 |  |  |  | n-4 |
| n-3 |  |  |  | n-3 |
| n-2 |  |  |  | n-2 |
| n-1 |  |  |  | n-1 |
| n |  |  |  | n |

### Do you use Spare Area or not? (If yes, please reply Q4)

**Yes, the Spare Area is used.**

### Does the programming file you provide to Dediprog including the data stored in Spare Area? Or need Dediprog self-calculate this part? (If so, please reply Q5)

**Yes the programming file includes the Spare Area data with pre-calculated ECC data.**

### If Dediprog should self-calculate the data stored in Spare Area, please provide the data storage format.

**Does Not Apply**

### Please provide the ECC (Error Checking and Correction) algorithm (or the algorithm you use) to Dediprog.

**The algorithm that we use is BCH16.**

**The programming file includes the calculated ECC data in the Spare Area.**

### Please describe the details if there is any text system. Ex: FAT16, FAT32, EXT2, EXT3 … etc.

**None**

### Please fill below column if there is any programming content for fixed position. And, should Dediprog programmer verdict programmed fail when there is a bad block on the fixed position?

**Programming should fail if there are bad blocks in any of the first six blocks.**

|  |  |  |  |
| --- | --- | --- | --- |
| Block | Flash Address | Size (number of block) | Content |
| 0 | 0x0 | Approximately 260 blocks | First time boot script and data. |
|  |  |  |  |
|  |  |  |  |

### Is there any specific data (Ex: Data mapping table, Buffer data… etc.) need Dediprog self-calculate and write into chip? If yes, please provide more details and advise it is Big or Small Endian.

**None**

### Please advise if there is any other data/file (Ex: programming file version … etc.) which is not included in programming file need to be programmed into chip as well. If the data length is more than 1 byte, please advise it is Big or Small Endian.

**None**

### Is there any other programming data need to be aware or calculated by Dediprog? If yes, please describe and provide more details for the same.

**None**

# Revision History

|  |  |  |
| --- | --- | --- |
| Date | Version | Changes |
| 2013/07/29 | 1.01 | First Released |
| 2015/04/22 |  | Questionnaire completed by Matt Schuckmann  e-mail: marques.girardeli@planar.com |

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Printed in Taiwan.